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[Virtual shadow registers and virtual register windows - all 7 versions »](#)

EM Dowling - US Patent 6,799,269, 2004 - freepatentsonline.com

... If a next task to run has a higher priority than the current task, the scheduler checks to see if the shadow register empty flag is set, and if the shadow ...

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[Method and apparatus for a guest to access a memory mapped device - all 2 versions »](#)

G Neiger, AV Anderson, SM Bennett, RS ... - US Patent 20,070,156,986, 2007 - freepatentsonline.com

... For example, where storage location 123 is a TPR, shadow-address field ... a 4-kilobyte virtual APIC page, such that a virtual copy of the task priority may be ...

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[PDF] [A new approach to real-time checkpointing - all 6 versions »](#)

A Cunei, J Vitek - ACM/Userix International Conference On Virtual Execution ..., 2006 - unix.org

... The net result is that a task that would normally ... the execution speed of high-priority mutators easily ... far about maintaining an up-to-date shadow copy of the ...

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[Integrated hardware and software task control executive - all 4 versions »](#)

JS Bronte - US Patent 6,061,709, 2000 - freepatentsonline.com

... saved in task SHADOW registers SHADOW registers Low ... to the regular task control block's copy of the ... the hardware executive interrupt or task can indirectly ...

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[Method and system for shadowing file system structures from multiple types of networks - all 3 versions »](#)

S Pardikar - US Patent 5,721,916, 1998 - freepatentsonline.com

... The agent 44 performs this task as a background thread. ... queue and then proceeds down to lower priority entries. ... of the file by grabbing a shadow copy of the ...

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[Shadow memory system for storing variable backup blocks in consecutive time periods - all 2 versions »](#)

JE Davis - US Patent 4,959,774, 1990 - freepatentsonline.com

... 364/200, Multiple task oriented processor. ... that might be typically required to completely copy all of ... above a data storage system with a shadow memory subsystem ...

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[Method and apparatus for selecting a register file in a data processing system - all 2 versions »](#)

WC Moyer, J Arends - US Patent 5,812,868, 1998 - Google Patents

... sequence, data processor 10 saves a copy of a ... readily accessible when a high-priority task is entered ... bits also indicate whether exception shadow registers are ...

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[Interruptible digital signal processor having two instruction sets - all 3 versions »](#)

JAJ Leijten, MJG Bekooij, AJ Bink, JSH Van ... - US Patent 7,082,518, 2006 - Google Patents

... priority task implemented as an interrupt service routine ... The shadow buffer is used

to take a copy of the processor state at the 20 interrupt handling and ...

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DIGITAL SIGNAL PROCESSING APPARATUS - all 5 versions »

JAJ Leijten, MJG Bekooij, AJ Bink, JSH Van ... - EP Patent 1,368,732, 2008 - [freepatentsonline.com](#)

... In the latter case, the shadow buffer contents are saved to ... in a data memory, and a new copy of the ... in order to save the state information during task switching ...

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S Parent, S Changes, G Info - [gauss.ffil.org](#)

... The agent 44 performs this task as a background thread. ... queue and then proceeds down to lower priority entries. ... of the file by grabbing a shadow copy of the ...

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Key authors: [E Dowling](#) - [J Davis](#) - [K Moore](#) - [J Cong](#) - [J Bobba](#)



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